



# ARIES DEVELOPMENT BOARD Model v2.0

## OVERVIEW

The ARIES v2.0 is a fully indigenous and a “Made in India” product to get started with basic microprocessor programming and embedded systems. This board is built upon a RISC-V ISA compliant THEJAS32 SoC (ET1031 VEGA Processor) with easy-to-use hardware and software. The VEGA SDK also provides full ecosystem with numerous examples and support documentation.

## SPECIFICATIONS

- SoC : THEJAS32 \*(32- bit RISC-V)
- Clock Speed : 100 MHz
- Input Voltage : 7 V – 12 V
- I/O Voltage : 3.3 V
- SRAM : 256KB
- Flash : 2MB
- Peripherals : 3xUART, 3xSPI, 2xI2C, 3xTimer
- Analog Input : 4 Channels
- Digital I/Os : 8xPWM  
32xGPIOs (23xGPIO, 2xDIP, 2xPB, 2xLEDs, 1xRGB LED)
- Dimension : 78mm X 66mm



## APPLICATIONS

- Sensor fusion
- Smart Meter
- System supervisors
- Remote sensors
- Small IoT devices
- Wearable devices
- Motor drives
- Electronic Toys
- Electronic education devices
- Inverters
- Industrial networking
- Legacy 8/16-bit applications

\*The First RISC-V VEGA SoC Indigenously Developed by C-DAC



**Microprocessor Development Programme**

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