



DOT DEVELOPMENT BOARD MODEL v1.0

OVERVIEW

ARIES DOT v1.0 is a circular development platform based on indigenously developed THEJAS32 SoC which integrates DIR-V VEGA ET1031 Microprocessor which is specifically designed for wearable applications. The board can be directly connected to wearable devices. It can be connected to PC using a micro USB cable. The two layer development board is of diameter 5 cm.

SPECIFICATIONS

- SoC : THEJAS32 * (32-bit RISC-V)
- Clock speed : 100 MHz
- Input voltage : 5 V DC
- I/O Voltage : 3.3 V
- SRAM : 256 KB
- Flash : 2 MB
- Peripherals : 2 x UART, 1 x I2C, 2 x SPI
- Digital I/Os : 14 x GPIO, 8 x PWM, 1 x LED
- ADC inputs : 4 nos
- Dimension : 5 cm diameter



APPLICATIONS

- Wearable devices
- Gesture controlled devices
- Health monitoring devices
- Remote sensors
- Small IoT devices
- Electronic Toys
- Legacy 8/16 bit applications

***The First RISC-V VEGA SoC Indigenously Developed by C-DAC**



Digital India RISC-V (DIR-V) Program
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