VEGA
PROCESSOR
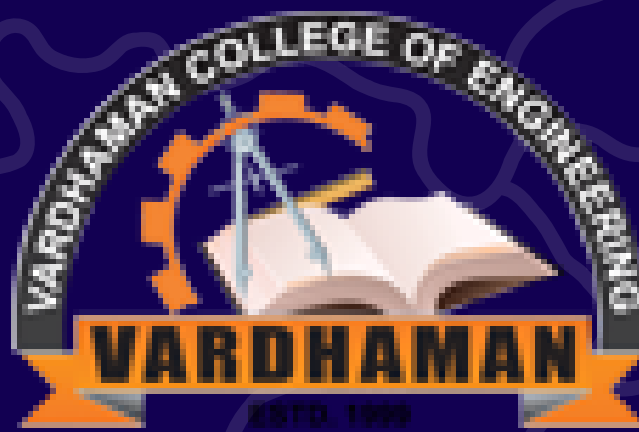
NATIONWIDE ROADSHOW ON DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

NOVEMBER 17 - 18, 2023
REGIONAL VENUE

Vardhaman College of Engineering
Hyderabad

One of the 15 venues in pan India event

For any queries, contact

Faculty Incharge

Register here by QR code:
Last Date: 25th Oct 2023

Dr. Sangeeta Singh
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Student Co-ordinators

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Every participant of the two-day workshop during the roadshow will receive a certificate from IEEE India Council and C-DAC
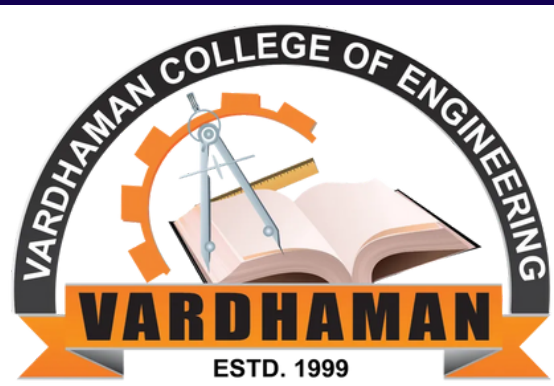
▶ @VEGAProcessors   in @vegaprocessor   🐦 @VegaProcessor

IEEE Sensors Council   IEEE VCE SB   C-DAC   Digital India RISC-V   MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY   VEGA PROCESSOR   IEEE INDIA COUNCIL

DIR-V VEGA MICROPROCESSOR

DIR-V VEGA Microprocessors is a series of indigenous microprocessors developed by C-DAC, MeitY, Govt. of India under the Digital India RISC-V (DIR-V) program. The VEGA series comprises of 32/64-bit Single/Dual/Quad Core superscalar Out-of-Order high performance processors based on RISC-V Instruction Set Architecture. The first VEGA processor based SoC chip THEJAS32, a 32-bit Single core SoC has been successfully fabricated. A fully indigenous and "Made in India" ARIES development platform based on THEAJS32 chip has been made widely available, targeted for both industry and academia involving embedded system design and IoT applications.

ABOUT THE COLLEGE

Vardhaman College of Engineering (VCE) was established in the year 1999 by Vardhaman Educational Society, Hyderabad. It is a UGC Autonomous college, approved by AICTE, and is permanently affiliated to Jawaharlal Nehru Technological University (JNTU), Hyderabad. We offer undergraduate B.Tech programmes in CSE, CSE (AI&ML), AI&ML, AI&DS, IT, ECE, EEE, ME and CE; postgraduate M.Tech programmes with specialisation in DECS, PEED, CSE, SE, and ED; MBA programme; and doctoral programmes in CSE, ECE and ME.
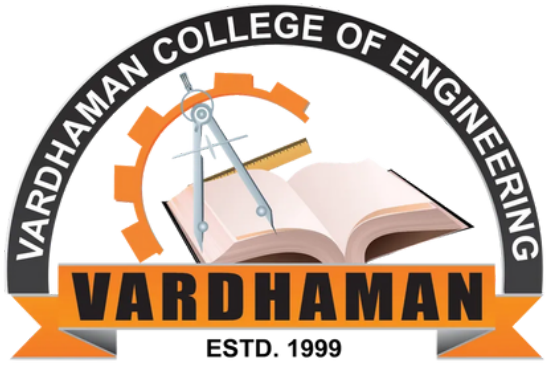
The college is accredited by NAAC at A++ grade with a CGPA of 3.58 on a scale of 4. Six of its UG engineering programmes – B. Tech ECE, EEE, CSE, ME, CE, and IT are accredited by the National Board of Accreditation (NBA), New Delhi under the Tier-1 category. VCE is the most sought-after institute to make the dreams fulfilled for many aspiring engineers. Our major strength lies in imparting quality education to the global standards and envisages to address various societal needs.

ABOUT ECE DEPARTMENT

The Department of Electronics and Communication Engineering has been playing a vital role in producing quality engineers ever since it was established in the year 1999. The department runs one undergraduate program and one postgraduate program to cater to the ever-changing needs of technical excellence in all areas of Electronics and Communication Engineering such as VLSI Design, Embedded Systems, Telecommunications, Signal Processing etc. The intake for the undergraduate Program (B. Tech) is 180. The department also offers Post Graduation programs with specialization in Digital Electronics and Communication Systems (DECS) with an intake of 18 .

ABOUT THE EVENT

The DIR-V VEGA nationwide roadshow, jointly organized by IEEE India Council and C-DAC, MeitY, will be held concurrently at various centers across India. This comprehensive training program, encompassing hands-on sessions, offers a deep dive into the VEGA series of processors and ecosystem. The training covers both theoretical foundations and practical applications, including insights into ARIES development boards, SDK and application development. Experts from C-DAC, integral members of the VEGA Processor development team, will deliver the training sessions. This workshop is designed for a diverse audience, including students, faculty members, research scholars, and industry professionals. Certificates will be awarded upon successful completion of the workshop.

Agenda

- Central Inauguration of Roadshow from MeitY
- Technical Sessions from Industry Experts
- Two-day hands-on workshop on the VEGA Processor and Ecosystem
- Familiarization of the ARIES development board and sensor interfacing
- Demonstration of IoT application modules developed during the workshop
- Display and screening of cost-effective solutions developed using VEGA ecosystem

Deadline to Register: 25th October 2023

Payment Link will be sent to the Registered Participants during the Confirmation email.

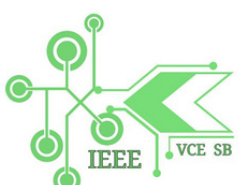**For more details & registration kindly visit the roadshow website**
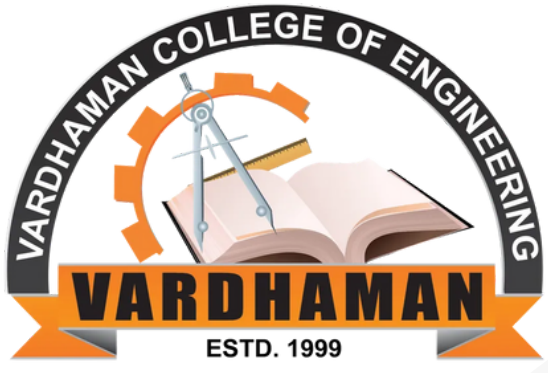https://vegaprocessors.in/dirv-roadshow2023/

**Registration Fees:
Rs. 400 (IEEE)
Rs. 500 (Non IEEE)**

@VEGAProcessors   @vegaprocessor   @VegaProcessor

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Patron
Dr. J V R Ravindra, Principal, VCE

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Dr. M.A. Jabbar, Branch Counselor, IEEE Vardhaman SB

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2. Dr. G.A.E. Satish Kumar, Dean, Mentoring, VCE
3. Dr. Ch. Sreenivasa Rao, Professor, ECE, VCE

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