

NATIONWIDE ROADSHOW ON DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

NOVEMBER 17 - 18, 2023

VELLORE INSTITUTE OF TECHNOLOGY (VIT) CHENNAI



Vellore Institute of Technology

(Deemed to be University under section 3 of UGC Act, 1956)

CHENNAI

Convenors

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Pro-Vice Chancellor, VIT Chennai

Dr. K. Porkumaran

Chairman, IEEE Madras Section Principal, Sri Sairam Engineering College, Chennai



Coordinators

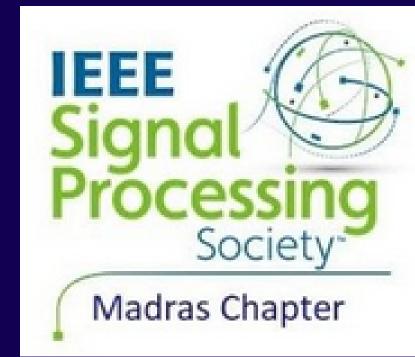
Dr. D. Vydeki

Professor, VIT Chennai

Dr. M. Jagannath

Professor, VIT Chennai

Dr. M. Arun



Chairman, IEEE Young Professionals Madras Section

Every participant of the two-day workshop during the roadshow will receive a certificate from IEEE India Council and C-DAC

















NATIONWIDE ROADSHOW ON DIR-V VEGA PROCESSORS

ABOUT THE EVENT

The DIR-V VEGA nationwide roadshow, jointly organized by IEEE India Council and C-DAC, MeitY, will be held concurrently at various centers across India. This comprehensive training program, encompassing hands-on sessions, offers a deep dive into the VEGA series of processors and ecosystem. The training covers both theoretical foundations and practical applications, including insights into ARIES development boards, SDK and application development. Experts from C-DAC, integral members of the VEGA Processor development team, will deliver the training sessions. This workshop is designed for a diverse audience, including students, faculty members, research scholars, and industry professionals. Certificates will be awarded upon successful completion of the workshop.

ABOUT VIT CHENNAL

VIT was established by Dr. G. Viswanathan, Founder and Chancellor, and a visionary who has transformed VIT into a centre of excellence in higher technical education. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. NAAC has re-accredited VIT with an 'A++' grade. VIT - The 8th best University, the 11th best research institution and the 11th best engineering institution in India (NIRF Ranking, Govt. of India 2023). Engineering and Technology subject areas of VIT are the 240th best in the World and the 9th best in India (QS World University Rankings by Subject 2023). VIT Chennai was established in 2010 and is presently headed by Dr. Sekar Viswanathan, Vice President of VIT. VIT Chennai is a globally engaged, competitive, comprehensive and research-enriched campus strategically positioned in the capital city of Tamil Nadu, to respond to major industrial, social, economic and environmental demands and challenges.



Registration Fee

IEEE Member - Rs. 300 + 18% GST Non-IEEE Member - Rs. 500 + 18% GST

For more details and registration kindly visit the roadshow website

Contact Details

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https://vegaprocessors.in/dirv-roadshow2023/

Last Date to Register: October 25, 2023















