



NATIONWIDE ROADSHOW ON DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

NOVEMBER 17 - 18, 2023

PAN-INDIA
WORKSHOP

REGIONAL VENUE



KONERU LAKSHMAIAH EDUCATION FOUNDATION

Sir C V RAMAN NEW SEMINAR HALL

Organised by

Department of Electrical and Electronics Engineering

Department of Computer Science and Engineering

15 REGIONAL VENUES



Every participant of the two-day workshop during the roadshow will receive a certificate from IEEE India Council and C-DAC

@VEGAProcessors

@vegaprocessor

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इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
ELECTRONICS AND
INFORMATION TECHNOLOGY



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NATIONWIDE ROADSHOW ON DIR-V VEGA PROCESSORS

ABOUT THE EVENT

The DIR-V VEGA nationwide roadshow, jointly organized by IEEE India Council and C-DAC, MeitY, will be held concurrently at various centers across India. This comprehensive training program, encompassing hands-on sessions, offers a deep dive into the VEGA series of processors and ecosystem. The training covers both theoretical foundations and practical applications, including insights into ARIES development boards, SDK and application development. Experts from C-DAC, integral members of the VEGA Processor development team, will deliver the training sessions. This workshop is designed for a diverse audience, including students, faculty members, research scholars, and industry professionals. Certificates will be awarded upon successful completion of the workshop.

DIR-V VEGA PROCESSOR

VEGA Processors is a series of indigenous microprocessors developed by C-DAC, MeitY, Govt. of India under the Digital India RISC-V (DIR-V) program. The VEGA series comprises of 32/64-bit Single/Dual/Quad Core superscalar Out-of-Order high performance processors based on RISC-V Instruction Set Architecture. The first VEGA processor based SoC chip THEJAS32, a 32-bit Single core SoC has been successfully fabricated. A fully indigenous and "Made in India" ARIES development platform based on THEAJ32 chip has also been made available widely, targeted for both industry and academia involving embedded system design and IoT applications.

AGENDA

- * Central Inauguration of Roadshow from MeitY
- * Technical Sessions from Industry Experts
- * Two-day hands-on workshop on the VEGA Processor and Ecosystem
- * Familiarization of the ARIES development board and sensor interfacing
- * Demonstration of IoT application modules developed during the workshop
- * Display and screening of cost-effective solutions developed using VEGA ecosystem



For more details & registration
kindly visit the roadshow website

<https://vegaprocessors.in/dirv-roadshow2023/>

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IMPORTANT DATES

Last Date for the Participants to Register for the Workshop : 25.10.2023

Announcement of the First List of Participants : 27.10.2023

Last date for Paying Registration Fee : 30.10.2023

Announcement of the Second List of Participants : 31.10.2023

Last date for Paying the Registration Fee : 03.11.2023

Nationwide VEGA Workshop at Regional Venues : 17.11.2023 - 18.11.2023

Certificate Distribution to Participants : 30.11.2023

REGISTRATION LINK

<https://forms.gle/bTg5V1KEs98opaVv9>

[Last Date : Wednesday, 25.10.2023]



Scan QR code for registration

Registration Fee

*IEEE Professionals/Faculty/
Research Scholars/UG&PG Students : Rs. 400/-+ GST*

Non-IEEE Members/ Industry Professionals : Rs. 600/- + GST

HOW CAN YOU ATTEND?

The roadshow is open to a diverse range of participants, including students, research scholars, faculty members, and industry professionals who are eager to explore indigenous microprocessors and their capabilities.

" Limited seats are available at regional venues for the workshop, so register quickly to secure your spot. Shortlisted candidates will be informed by the regional venue coordinators through email for the registration fee payment and seat confirmation. A nominal registration fee may be applicable. Registration fee discount is available for IEEE members."



ABOUT KONERU LAKSHMAIAH EDUCATION FOUNDATION

Established in 1980, KLEF is a pioneer in the field of Engineering education and research in India. K L E F has been declared as Deemed to be University by MHRD, Government of India and accredited by National Assessment and Accreditation Council (NAAC) as “A++” grade University. It is recognized by the Department of Scientific and Industrial Research (DSIR). It also has collaborations for conducting joint research with several institutes around the globe. It received “The best Educational Institute Award in Engineering” by Asian Confederation of Business. In 2019 UGC, MHRD declared this institution as Category I. The University was also ranked 28 under NIRF 2023.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

The Department of Electrical and Electronics Engineering at Koneru Lakshmaiah Education Foundation (KLEF) was started in the year 1995. Over the last few decades, grown our expertise and competence in the core Electrical and Electronics Engineering curriculum and research. We have undergraduate program in Electrical and Electronics Engineering, B.Tech, M.Tech. & dual degree programs. We have Full time/part time under research program, at present the department has more than 100 PhD scholars. The department offers design and outcome based engineering curriculum. The department maintains active research groups for carrying out collaborative and interdisciplinary research. We have state of art research facilities to support our academic programs blended with research. Sponsored funding projects worth of approx. 2.5 crores during last five years from DST-SERB & AICTE etc., to maintain and modernize our research infrastructure. The department’s faculty have published 1500 plus research articles in referred SCIE/WoS/Scopus etc., 42 patents with 2 granted in IPR and 23 technical textbooks in standard publishers. The department has strong industry connect like MNC’s and top public and private industries to bridge the gap between academia and industry. The faculty members got many prestigious awards at national and international levels. The department looks forward to contributing in solving the technological challenges of the society with active participation from all sections.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

The Department of CSE has started in the year 1980 and has grown up exponentially in size and quality as one of the enriched departments in the two telugu speaking states of Andhra Pradesh & Telangana. Currently the department has 459 faculty members with more than 50% of them being doctoral degree holders with vast experience in academia, research and industry which helps impart quality education to the students in various specialized domains of Computer Science and Engineering at both undergraduate and post graduate level including the doctoral programs. The faculty expertise in the department drives the teaching learning process effectively and efficiently for both advanced and slow learners. The 15:1 student to faculty ration helps counsel students personally towards their academic goals and career goals on a regular basis.

Please visit the event website for updates

<https://vegaprocessors.in/dirv-roadshow2023>

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