

# NATIONWIDE ROADSHOW ON DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

November 17-18, 2023

## AGENDA

NOV 17

09:30 - 09:40	Welcome Address	Mr. Magesh E, Director General, C-DAC
09:40 - 9:50	The DIR-V Journey	Ms. Sunita Verma, Scientist G & Group Coordinator, MeitY, Govt. of India
09:50 - 10:00	Special Address	Ms. Calista Redmond, CEO, RISC-V International
10:00 - 10:15	Tech Talk	Prof. V. Kamakoti Director, IITM & Chief Architect, DIR-V Program
10:15 - 10:30	Inaugural Address	Mr. Rajeev Chandrasekar, Hon'ble Minister of State, MeitY & MSDE, Govt. of India
10:30 - 10:35	Felicitation	Dr. Debabrata Das Director, IIIT Bangalore & Chair of IEEE India Council
10:35 - 10:40	Felicitation	Dr. John Jose Professor, IIT Guwahati & Vice Chair, IEEE India Council
10:40 - 11:00	Technical presentation	Prof. Krste Asanovic, Chief Architect, SiFive & Professor at UC Berkeley
11:00 - 11:20	Technical presentation	Mr. Alexander Redkin, Director, Syntacore
11:20 - 11:40	Technical presentation	Mr. Balaji Baktha, Founder and CEO, Ventana Micro Systems
11:40 - 11:55	DIR-V VEGA Processors and Ecosystem	Mr. S. Krishnakumar Rao, Senior Director, C-DAC & Program Manager, DIR-V Program
11:55 - 12:00	Vote of Thanks	Ms. Neetha Maria Celin, Joint Director, C-DAC
12:00 - 13:00	Programme @ Regional Venues	
13:00 - 14:00	Lunch Break and Networking	
14:00 - 17:00	Hands-on workshop @ Regional Venues	

NOV 18

09:00 - 17:00	Hands-on workshop @ Regional Venues
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